

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2664022	controller or (control\$4 adj4 (device or circuit\$2 or board or interfac\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:28
L2	2147158	interfac\$3 or card or chip	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:28
L3	5938650	mux\$3 or multiplex\$3 or switch\$3 or select\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:29
L4	3087802	master or host or cpu or processor or microprocessor or computer or microcomputer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:30
L5	71585	2 with 3 with 4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:31
L6	3518	2 with 3 with target	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:32
L7	1277	5 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:32
L8	8509	(714/724,726,727,728,729,730,734,733,30,31,47,48).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:32
L9	5349	(370/351,241,242,244,245,216,217,221).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:32
L10	34	7 and (8 or 9)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/07/07 15:33